



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Jae-Eun LIM, et al.

Serial No.: 10/735,742

Group Art Unit: 2812

Filed: December 16, 2003

Examiner: Issac, Stanetta D

For: METHOD FOR MANUFACTURING SHALLOW TRENCH ISOLATION IN  
SEMICONDUCTOR DEVICE

\* \* \* \* \*

PETITION FOR EXTENSION OF TIME

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 17, 2004

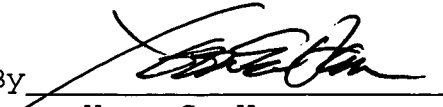
Sir:

Applicant hereby petitions that an extension of the term necessary to render timely the filing of a response in the above-identified application be granted. Costs under 37 CFR 1.17(a) - (d) for such extension(s) and/or any other fee due with this response, not fully covered by the enclosed check in the amount of \$120.00, may be charged to our Deposit Account No.50-3226.

Respectfully submitted,

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